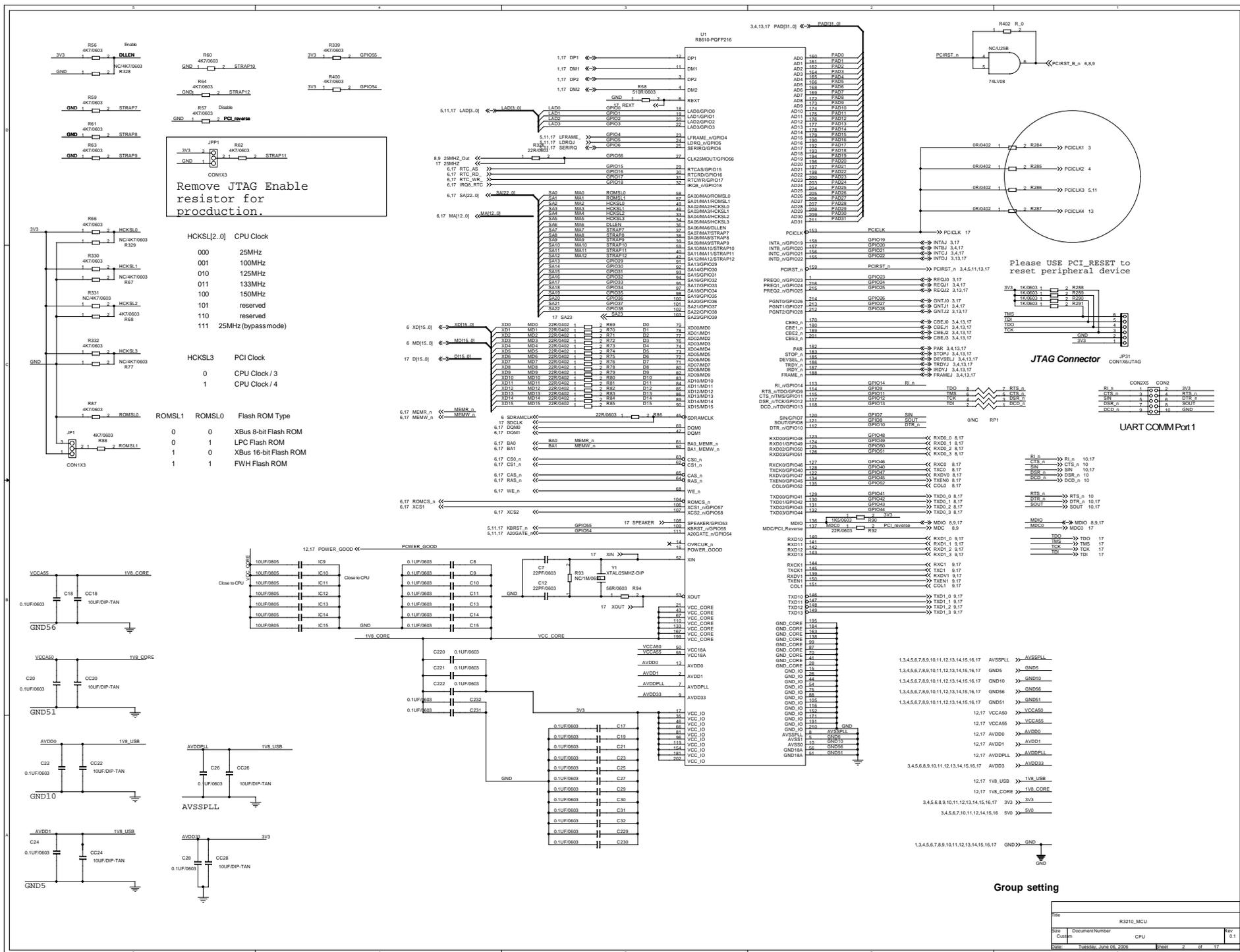


for EMI supression
(CONNECT TO CHASSIS GND)

DATE	REVISION NUMBER	DESCRIPTION
1/21/05	R8610-MCU-050121	1. Initial release.
03/31/05	R8610-MCU-050331	1. PCI -solt change to Device 1 that idsel connect to PAD12 2 Mini PCI change to Device 2 that idsel connect to PAD 13 3.ITE-IDE change to Device 4 that idsel connect to PAD15 4.Pull_Down CLKRUNJ 5. Seperate the JTAG and UART multi-function pin
04/21/06	R8610-BGA-QFP-042106	1. Remove U25. ROMCS can support 16M. 2.Add note "Remove JTAG Enable resistor for production."

Title		
R3210_MCU		
Size B	Document Number	Rev 0.1
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Remove JTAG Enable resistor for production.

- HCKSL[2..0] CPU Clock**
- 000 25MHz
 - 001 100MHz
 - 010 125MHz
 - 011 133MHz
 - 100 150MHz
 - 101 reserved
 - 110 reserved
 - 111 25MHz (bypassmode)
- HCKSL3 PCI Clock**
- 0 CPU Clock / 3
 - 1 CPU Clock / 4
- ROMSL1 ROMSLO Flash ROM Type**
- 0 0 XBus 8-bit Flash ROM
 - 0 1 LPC Flash ROM
 - 1 0 XBus 16-bit Flash ROM
 - 1 1 FWX Flash ROM

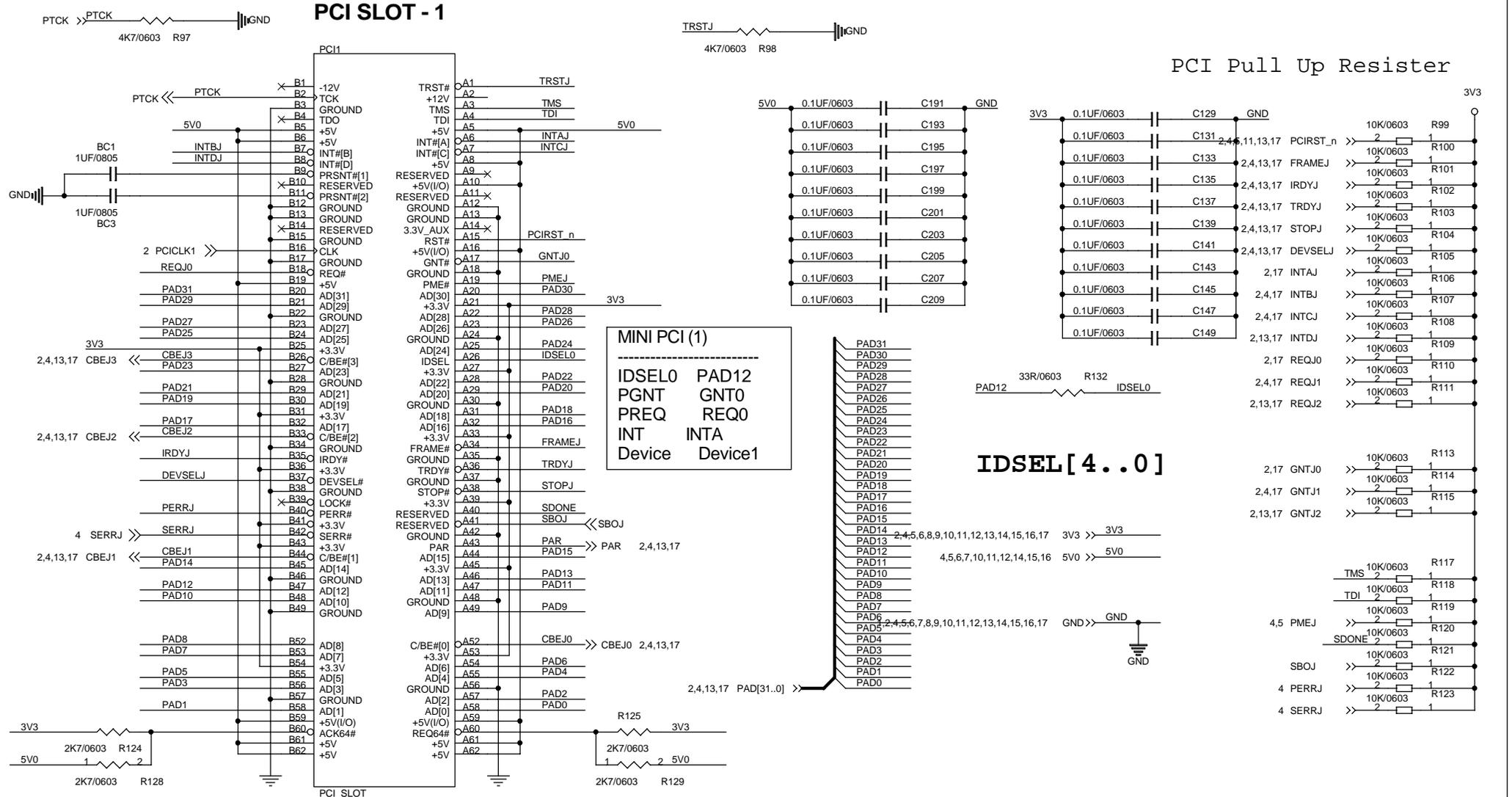
Please USE PCI_RESET to reset peripheral device

JTAG Connector

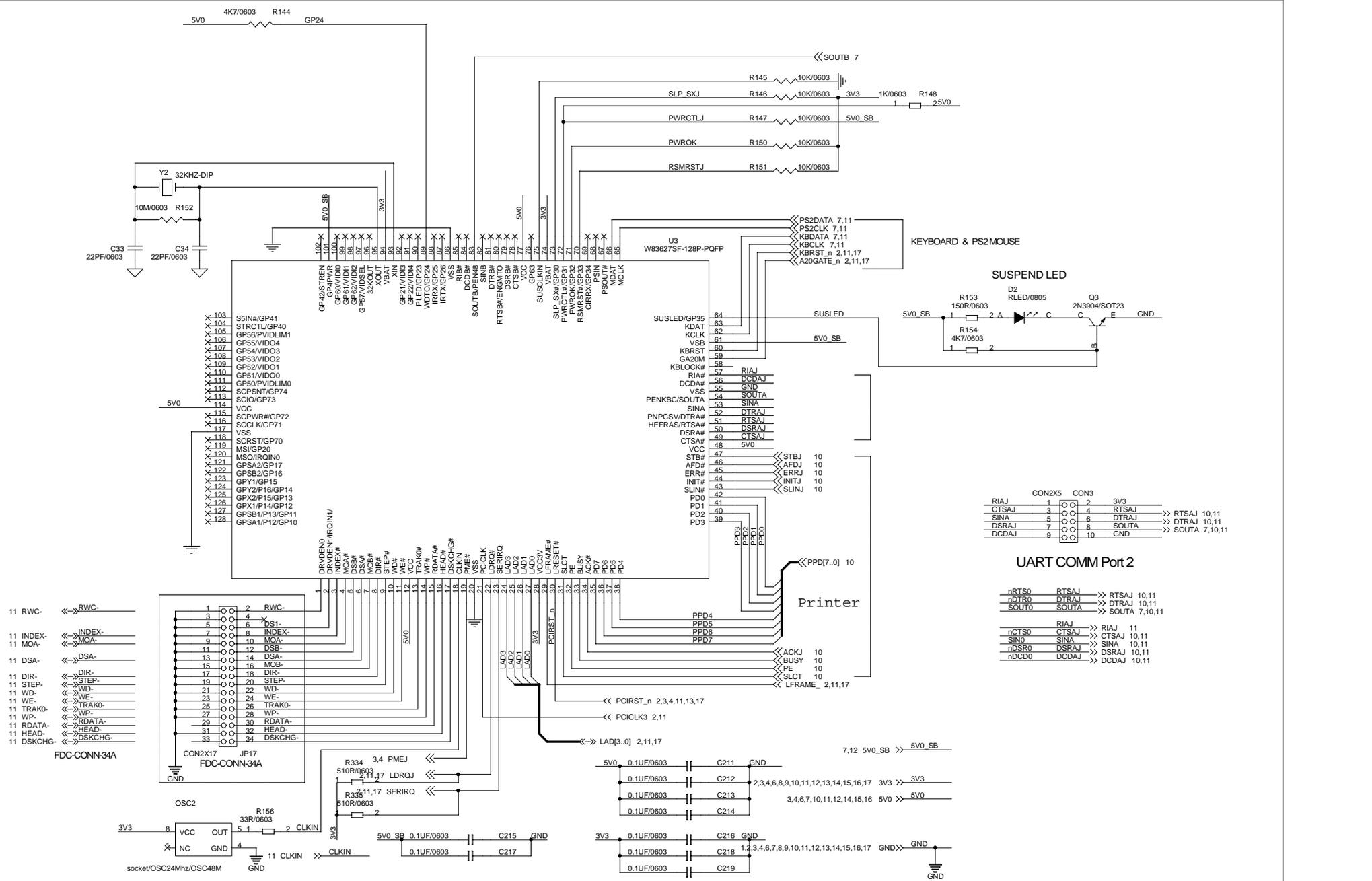
UART COMM Port 1

Group setting

File	R320_MCU
Doc Number	CPU
Rev	0.1
Date	Tuesday, June 06, 2006
Sheet	3 of 17



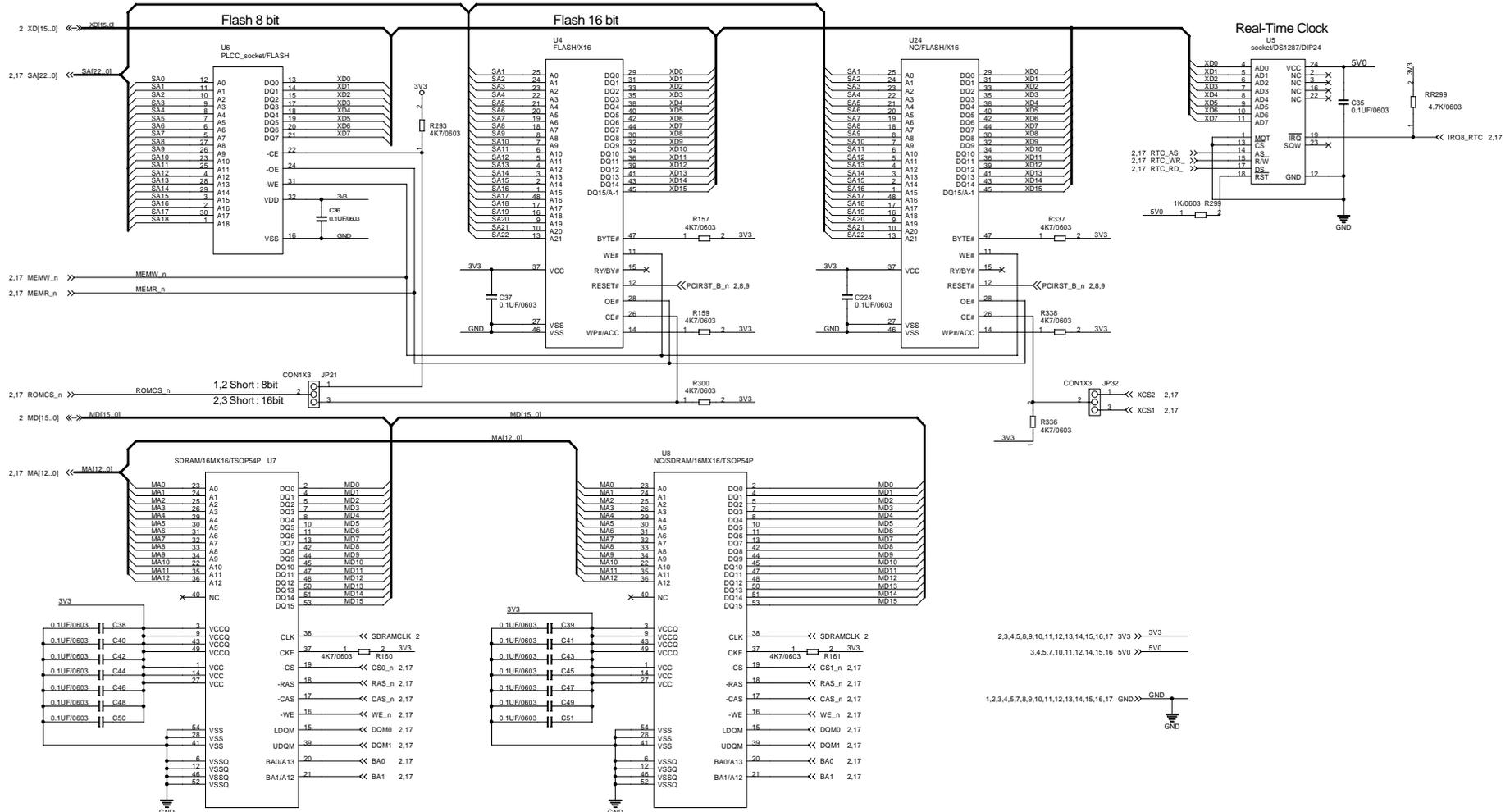
Title			R3210_MCU		
Size	Document Number	PCI_Slot_0,1		Rev	0.1
Custom					
Date:	Tuesday, June 06, 2006	Sheet	3	of	17



CON2X5	CON3	3V3
RIA# 1	2	RTSAJ
CTSAJ 3	4	DTRAJ
SINA 5	6	SOUTA
DSRAJ 7	8	GND
DCDAJ 9	10	GND

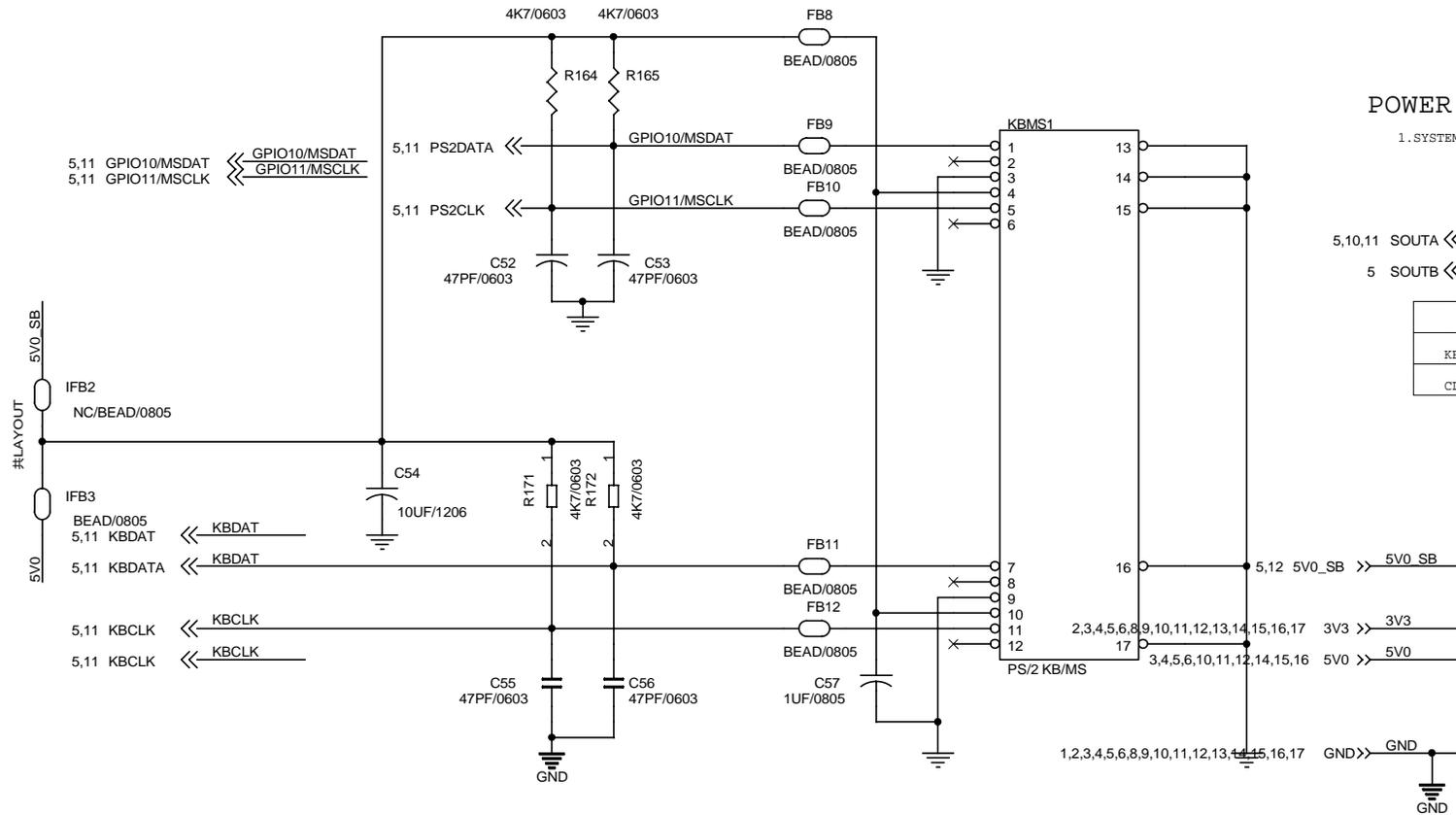
nRTS0	RTSAJ	nTR0	DTRAJ	SOUT0	SOUTA
nCTS0 <td>RTSAJ</td> <td>SINO <td>SINA</td> <td>nDSR0 <td>DSRAJ</td> </td></td>	RTSAJ	SINO <td>SINA</td> <td>nDSR0 <td>DSRAJ</td> </td>	SINA	nDSR0 <td>DSRAJ</td>	DSRAJ
nDCD0 <td>DCDAJ</td> <td></td> <td></td> <td></td> <td></td>	DCDAJ				

Title		
R3210_MCU		
Size	Document Number	Rev
Custom	LPC Super I/O	0.1
Date:	Tuesday, June 06, 2006	Sheet 5 of 17



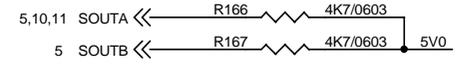
file	R3210_MCU	
Size	Document Number	Rev
C	Flash / SDRAM / RTC	0.1
Date	Tuesday, June 08, 2006	Sheet 6 of 17

Keyboard & Mouse connector



POWER ON SETTING PIN

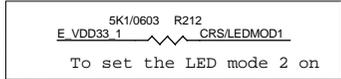
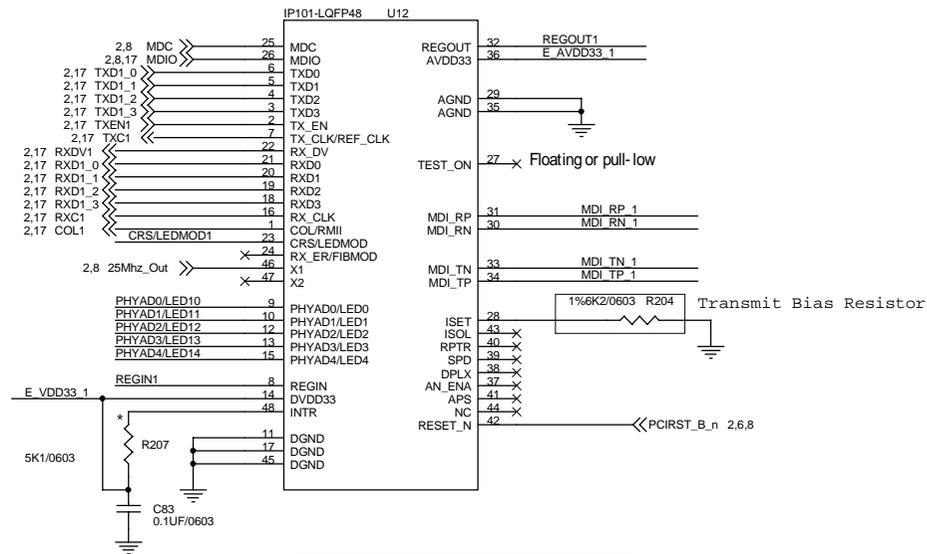
1. SYSTEM SETTING



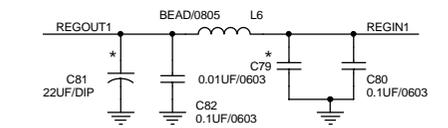
OFF	ON
KBC DISABLE	KBC ENABLE
CLK 24M	CLK 48M

PIN18 INPUT CLK VALUE

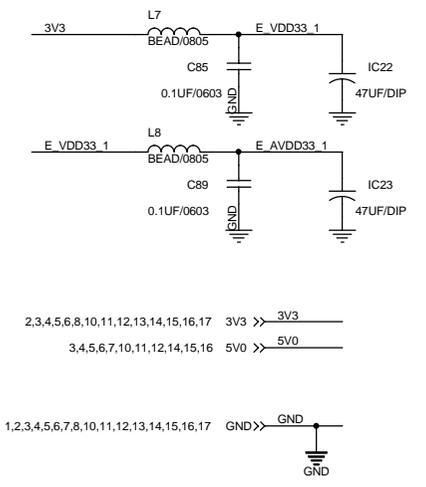
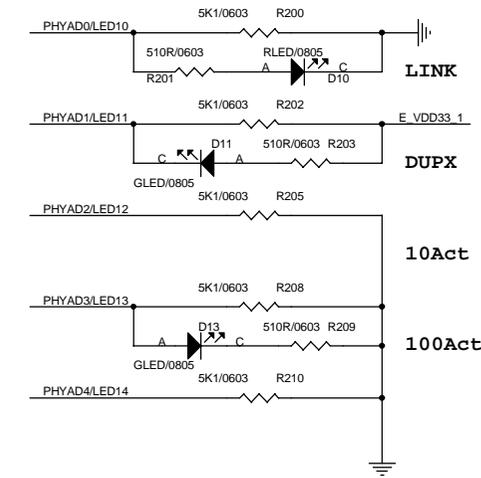
Title		
R3210_MCU		
Size	DocumentNumber	Rev
Custom	KB / Mouse / IR	0.1
Date:	Tuesday, June 06, 2006	Sheet 7 of 17



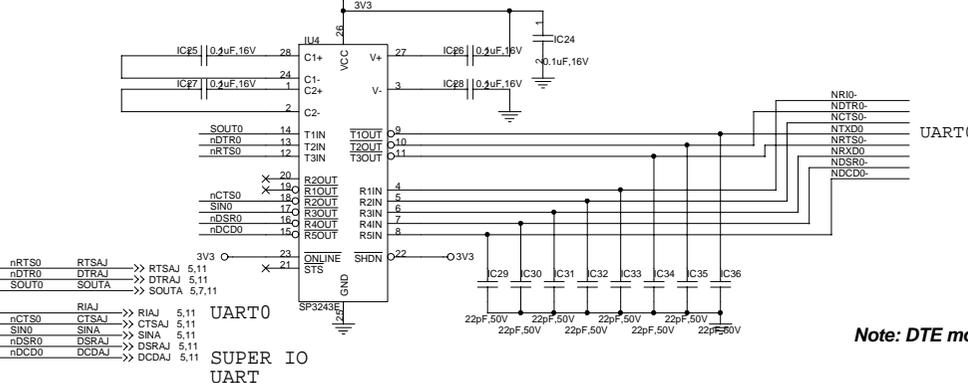
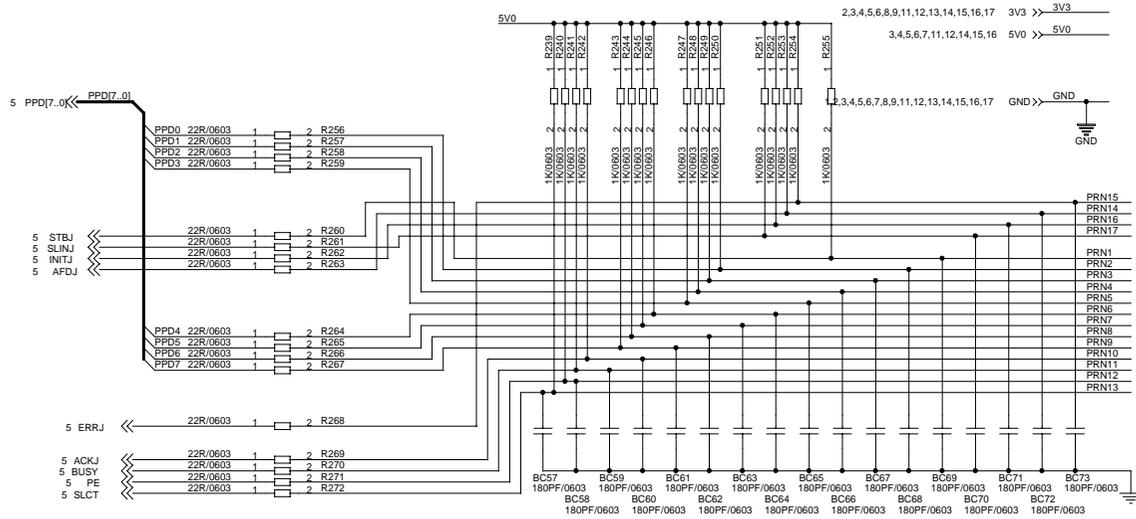
* : Optional ,but recommended
Pin48 could be short VDD33 if interrupt funtion is not used.



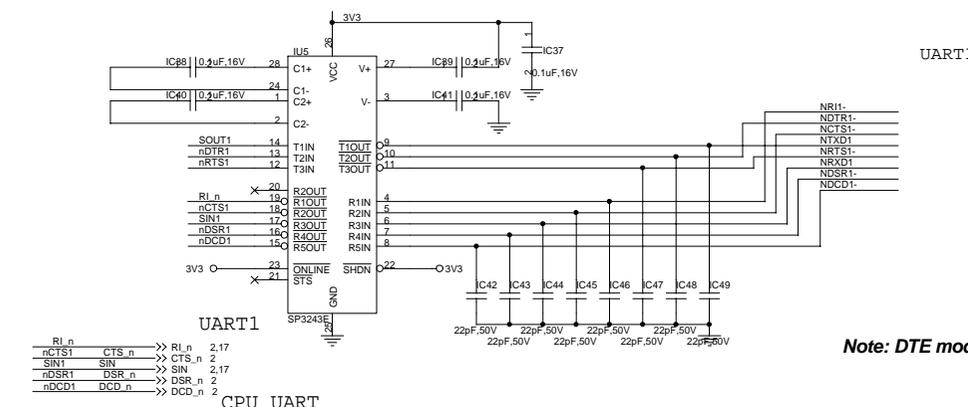
This schematic sets PHY address to 00010b.



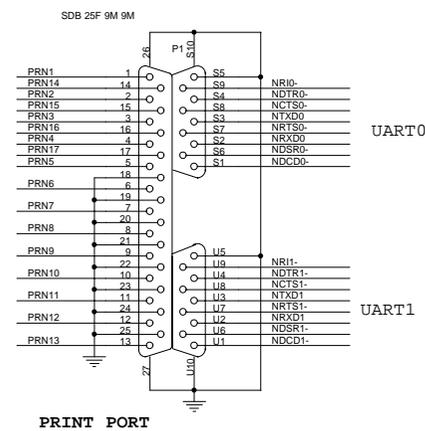
Title			R3210_MCU
Size	Document Number	MII / PHY1	
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			Rev 0.1



Note: DTE mode pin define



Note: DTE mode pin define



PRINT PORT

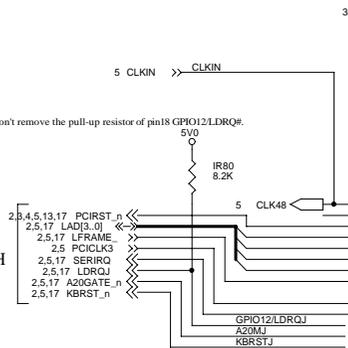
nRTS1 RTS_n >>> RTS_n 2
 nDTR1 DTR_n >>> DTR_n 2,17
 nSOUT1 SOUT >>> SOUT 2,17

File			R3210_MCU
Size	Document Number	Rev	0,1
Custom	PRN / GAME / MIDI		
Date:	Tuesday, June 06, 2006	Sheet	10 of 17

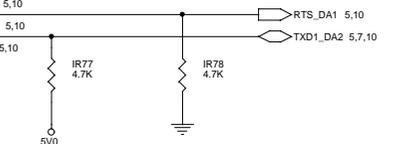
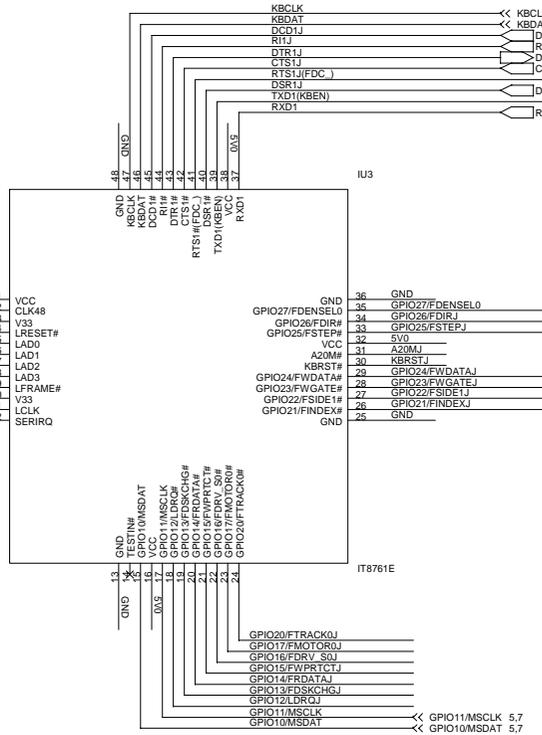
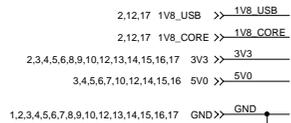
CLOCK Lines Layout/Routing Guidelines (Pin2:CLK48 & Pin11:LCLK)

- 1) If possible, please avoid using any through-hole.
- 2) Please make the trace length short, and the trace width wide enough.
- 3) The spacing to the closest neighbor should be wide enough.
- 4) The discrete damping resistors and capacitors are recommended.

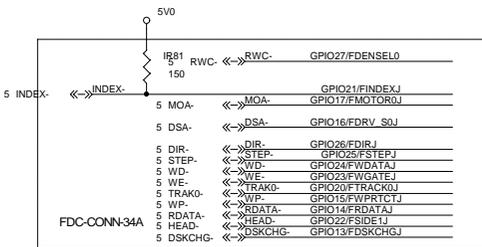
TO ICH



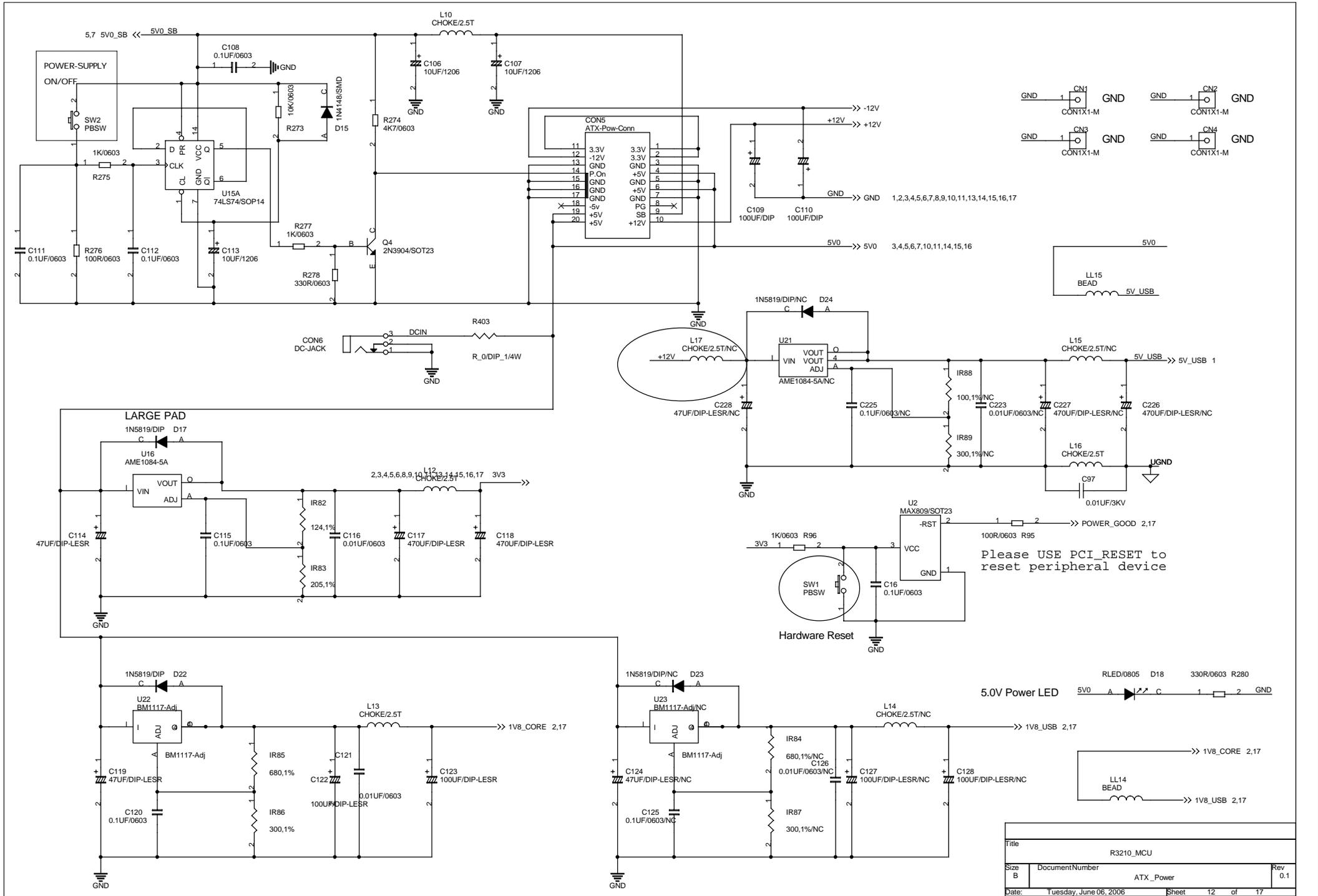
Please don't remove the pull-up resistor of pin18 GPIO12/LDRQ#.



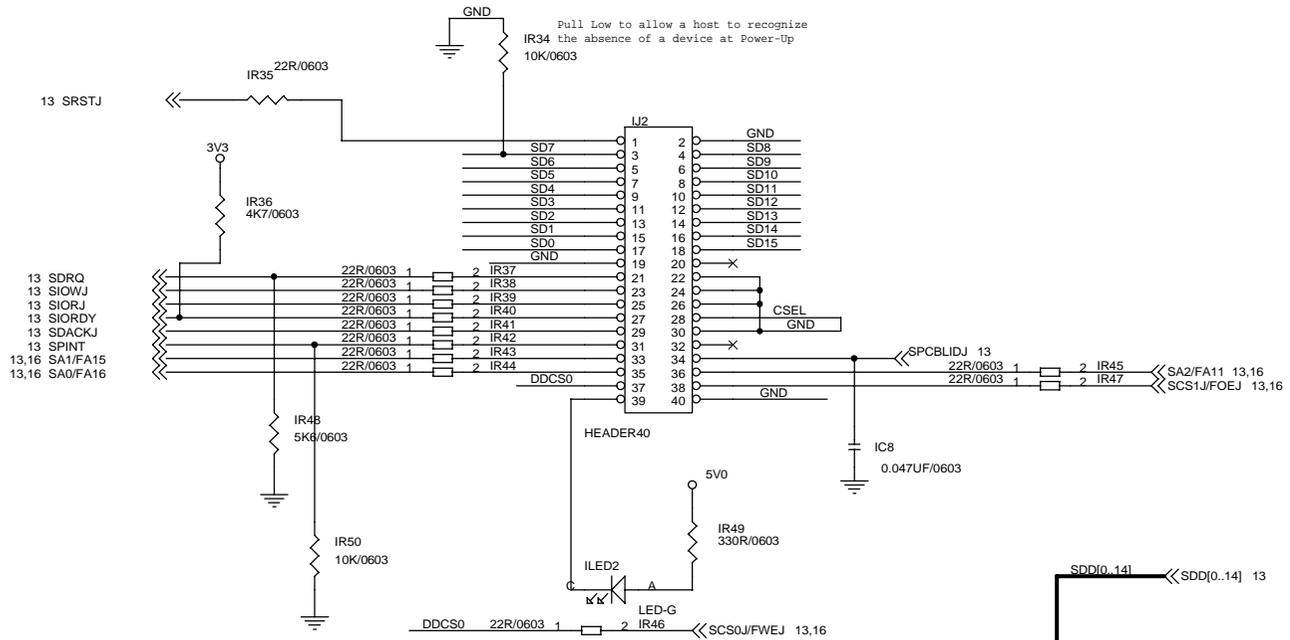
UART 0



Title		R3210_MCU	
Size	Document Number	Rev	
Custom	ITE-supri/O	<Rev Code>	
Date:	Tuesday, June 06, 2006	Sheet	11 of 17



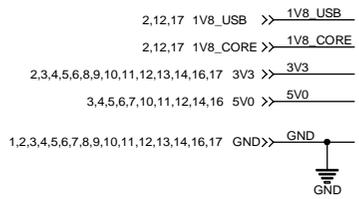
Title			R3210_MCU
Size	DocumentNumber	Rev	
B	ATX_Power	0.1	
Date:	Tuesday, June 06, 2006	Sheet	12 of 17



Pull Low to allow a host to recognize the absence of a device at Power-Up

SDD8	22R/0603	1	2	IR51SD8
SDD7	22R/0603	1	2	IR53SD7
SDD9	22R/0603	1	2	IR55SD9
SDD6	22R/0603	1	2	IR57SD6
SDD10	22R/0603	1	2	IR52SD10
SDD5	22R/0603	1	2	IR54SD5
SDD11	22R/0603	1	2	IR56SD11
SDD4	22R/0603	1	2	IR58SD4

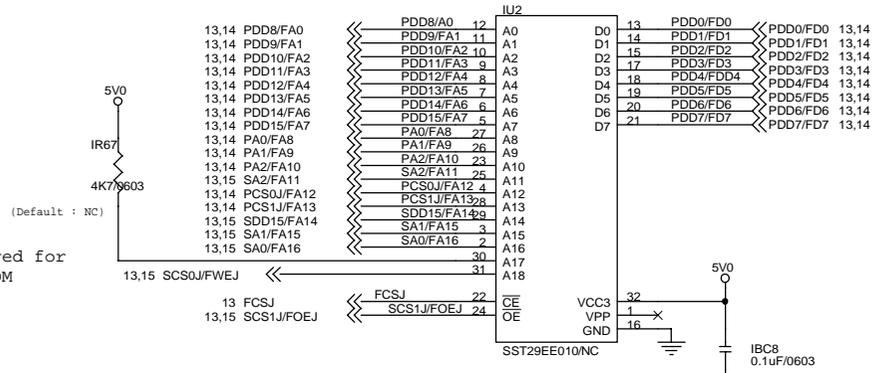
SDD12	22R/0603	1	2	IR59SD12
SDD3	22R/0603	1	2	IR61SD3
SDD13	22R/0603	1	2	IR63SD13
SDD2	22R/0603	1	2	IR65SD2
SDD14	22R/0603	1	2	IR60SD14
SDD1	22R/0603	1	2	IR62SD1
SDD15	22R/0603	1	2	IR64SD15
SDD0	22R/0603	1	2	IR66SD0



Title		
R3210_MCU		
Size B	Document Number	Rev 2.1
8212_IDE_CONNECTOR2		
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8BITS

Note:
R13 is reserved for
28 pin/DIP ROM
type.

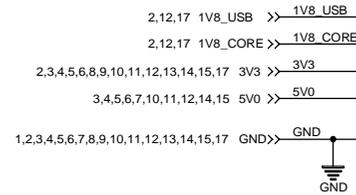


13 FCSJ

← FCSJ

Note :

1. Pull FCS# to H -> IT8212 is running at PCI 66 Mhz.
2. Pull FCS# to L -> IT8212 is running at PCI 33 Mhz.



Title		
R3210_MCU		
Size	Document Number	Rev
B	IDE-BootRom	<Rev Code>
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